Form PTO-1449 (modified)	Atty. Docket No. Serial No. 2000.056900/TT4089 10/047,188				
List of Patents and Publications for Applicant's INFORMATION DISCLOSURE STATEMENT		Applicant(s) Brian C. Barnes, Geoffrey S. Strongin,Rodney W. Schmidt			
(Use several sheets if necessary)		Filing Date: Group: 01/15/2002 2135		_	
U.S. Patent Documents	Foreign P	atent Documents		Other Art	
See Page	Se	ee Page 1	See Page 1		

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1	09/999,881		Barnes et al.	713	200	10/31/2001
	A2	10/047,188		Barnes et al.			01/14/2002
	A3	6,775,779	08-2004	England et al.			
	A4	6,477,612	11-2002	Wang, Landy	711	2	
	A5	5,293,597	03-1994	Jensen et al	711	207	
	A6	5,890,189	03-1999	Nozue et al.	711	100	
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	A8	5,469,556	11-1995	Clifton	711	163	
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Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1	2 356 469	05/23/2001	GB	G06F	1/100	

Examiner:	DATE CONSIDERED:

EXAMINER: INITIAL IF REFERENCE CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.

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(Use several sheets if necessary)		iling Date: 1/15/2002	Group: 2135	
U.S. Patent Documents See Page	· ·	nt Documents Page 1	Other Art See Page 1	

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C1	International Search Report dated July 7, 2003 for International application no. PCT/US02/25401, Filed August 9, 2002
	C2	Chiueh, Tzi-cker; Venkitachalam, Ganesh; Pradhan, Prashant; "Integrating Segmentation and Paging Protection for Safe, Efficient and Transparent Software Extensions;" XP-000919654, Computer Science Department, State University of New York at Stony Brook, 1999, pages 140-153.
	C3	Gilmont, T. et al; "Enhancing Security in the Memory Management Unit"; IEEE Xplore; Release 1.8; Euromicro Conference, 1999; 9 pgs.
	C4	Morris, et al.; "Fast Communication in Distributed Systems Using the Networked Virtual Memory System;" Fujitsu Labs., Ltd., July 1994; pp 1-6
	C5	Ozaki, et al.; "Software Fault Tolerance in Architectures with Hierarchical Protection Levels"; August, 1988 IEEE MICRO; pp. 30-43
	C6	Office Action dated 05/06/06 regarding S/N 09/999,881
	C7	Final Office Action dated 11/02/05 regarding S/N 09/999,881
	C8	Office Action Reopening Prosecution dated 08/08/06 regarding S/N 09/999,881
	С9	Office Action dated 02/08/07 regarding S/N 09/999,881
	C10	Office Action dated 08/10/07 regarding S/N 09/999,881
	C11	Final Office Action dated 02/06/08 regarding S/N 09/999,881
	C12	Office Action dated 03/15/05 regarding S/N 10/044,667
	C13	Final Office Action dated 09/30/05 regarding S/N 10/044,667
	C14	Office Action Reopening Prosecution dated 05/16/07 regarding S/N 10/044,667
	C15	Office Action dated 11/01/07 regarding S/N 10/044,667
	C16	Final Office Action dated 05/12/08 regarding S/N 10/044,667
	C17	Office Action 10/13/08 regarding S/N 10/044,667
	C18	
	C19	

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U.S. Patent Documents Foreign P		atent Documents	Other Art	
See Page	Se	ee Page 1	See Page 1	

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C20	
	C21	

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